

# LT8218 --- Product Brief

## LVDS Transmitter

### Features

- Wide dot clock range:25-160MHz suited for NTSC,VGA, SVGA, XGA,SXGA and SXGA+
- PLL requires no external components
- Supports spread spectrum clock generator
- On chip jitter filtering
- Clock edge selectable
- Power down mode
- Low profile 56 or 48 TSSOP Package
- Support 1.8V,2.5V and 3.3V RGB input
- ESD: HBM to 2KV

### Description

The LT8218 LVDS (Low Voltage Differential Signaling) transmitter is designed to support pixel data transmission between Host and Flat Panel Display from NTSC up to 1080p (60Hz).

The LT8218 converts 28 bits of CMOS/TTL data into LVDS data stream. The transmitter can be programmed for rising edge or falling edge clocks through a dedicated pin. At a transmit clock frequency of 160MHz, 24bits of RGB data and 4bits of timing and control data(HSYNC, VSYNC, DE, CNTL) are transmitted at an effective rate of 1120Mbps per LVDS channel.

### Applications

- LCD Display Panel Driver
- Digital Picture Frame
- UMPC and Netbook PC



Figure 1. Application Diagram

### Ordering Information

| Part Number | Operating Temperature Range | Package | Packing Method |
|-------------|-----------------------------|---------|----------------|
| LT8218      | -40°C to+85°C               | TSSOP56 | Tape and Reel  |

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