

LT86101UXS_U3Q10CEN --- Product Brief

HD-DVI2.0 TMDs Re-timer/Level Shifter

1. Features

- Support 480P,720P,1080P,4K@30Hz as specified in HD-DVI1.4
- Support 4K@60Hz as specified in HD-DVI2.0
- Programmable input equalization up to 20dB
- Programmable output swing and de-emphasis
- DC/AC couple input capable
- Support input data channel swap (arbitrarily) and polarity inversion (independent)
- Support output channel swap (arbitrarily) and polarity inversion (independent)
- Input signal jitter cleaning
- Integrated DDC interception
- Support HD-DVI ID by DDC
- Support HDCP repeater
- Automatic power management
- 1.2V and 3.3V power supply

- Integrated 100k/400kHz I2C slave
- Low power consumption

2. General Description

LT86101UXS_U3Q10CEN is HD-DVI2.0 TMDs Re-timer/Level Shifter. It supports up to 6Gbps TMDs signal with programmable equalization and pre-emphasis for better jitter performance. Both DC/AC couple input signals are capable. LT86101UXS_U3Q10CEN will automatically manager power consumption when source or sink is asserted or deserted. System control is also available through the use of a dedicated configuration I2C slave interface.

3. Applications

- PC Motherboards
- Docking Station
- Dongle

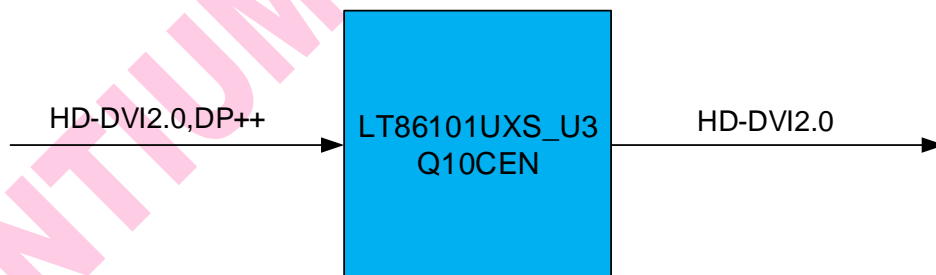


Figure 3.1 Application Diagram

4. Ordering Information

Table 4.1 Ordering Information

Part Number	Product Status	Package	Bonding Wire	Grade	Operating Temperature Range	Stack Die Option	Packing Method	MPQ
LT86101UXS_U3Q10CEN	MP	QFN48(6*6)Saw	Cu	E	-40°C to +85°C	N	Tray	4900pcs

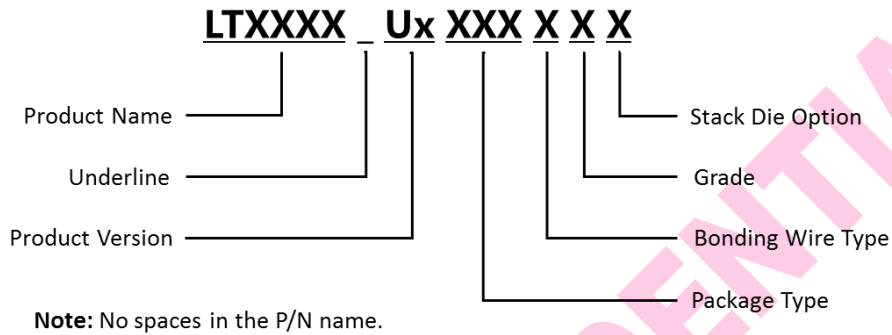


Figure 4.1 Part Number Naming Rules

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